

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

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TAKASHI YAMADA, ET AL.

: EXAMINER: DICKEY, T. L.

SERIAL NO: 09/995,594

RCE FILED: HEREWITH

: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR CHIP HAVING MULTIPLE FUNCTIONAL BLOCKS INTEGRATED IN A SINGLE CHIP AND METHOD FOR FABRICATING THE SAME

AMENDMENT

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated September 2, 2003, please amend the aboveidentified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.